

Multicore Navigator User Guide

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Multicore Navigator User Guide

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Multicore Navigator User Guide - gamma-ic.com

KeyStone Architecture Multicore Navigator User's Guide Literature Number: SPRUGR9H November 2010-Revised April 2015

KeyStone Architecture Multicore Navigator

ø-ii KeyStone Architecture Multicore Navigator User Guide SPRUGR9D+ + +—January 2012 www.ti.com Submit Documentation Feedback TI Confidential—NDA Restrictions Release History Release Date Description/Comments D+ + + January 2012 • Added Navigator Cloud note to Rx Flows. (Page 4-23) • Updated QMn Base Address Regs (Page 4-19)

Multicore Navigator User Guide - Texas Instruments

KeyStone Architecture Multicore Navigator User's Guide Literature Number: SPRUGR9G November 2010-Revised October 2014

KeyStone Architecture Multicore Navigator (Rev. G)

Read Free Multicore Navigator User Guide Multicore Navigator Overview The purpose of this User's Guide is to provide more detailed information regarding the software elements and infrastructure provided with MCSDK. MCSDK pulls together all the elements into demonstrable multicore applications and examples for supported EVMs. BIOS MCSDK 2.0 User Guide - Texas

Multicore Navigator User Guide - massagetherapie-inbeweging.nl

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[MOBI] Multicore Navigator User Guide

Definition. • Multicore Navigator is a collection of hardware components that facilitate data movement and multi-core control. • The major components within the Navigator domain are: - A hardware Queue Manager (QM). - Specialized packet DMAs, called PKTDMA. - Data structures to describe packets, called descriptors.

KeyStone Training Multicore Navigator Overview

Devices User Guide or the Multicore Navigator for KeyStone Devices User Guide. 2.10 Power Management The packet accelerator (PA) power is managed through the network coprocessor (NETCP) power domain and through disabling the clock that operates the PA logic. The PA shares its power domain with the ot her modules in the NETCP, and therefore,

Packet Accelerator (PA) for KeyStone Devices User's Guide ...

KeyStone Architecture Network Coprocessor (NETCP) User Guide: REF: 3: SPRUGR9 April 2011 KeyStone Architecture Multicore Navigator User Guide: REF: 4: SPRUGV9 November 2010 KeyStone Architecture Gigabit Ethernet(GbE) Switch Subsystem: REF: 5: SPRUGV4 March 2011 KeyStone Architecture Power Sleep Controller (PSC). REF: 6: SPRUGY6 May 2011

TransportNetLib UsersGuide - Texas Instruments Wiki

For more information on which peripherals required, please see the Multicore Navigator user guide . 2. Is it possible to disable Multicore Navigator entirely? [Derek] It depends on what you mean by "disable." From a functionality perspective, if the peripherals requiring the Multicore Navigator are not used, then it does not need to be setup ...

Multicore Navigator Question - Processors forum ...

• Multicore Navigator is a hardware mechanism that facilitates data movement and multicore co-working • Supports multiple users (players) - Each core in a multicore system - High bit-rate peripherals including Serial Rapid I/O (SRIO), Antennae Interface (AIF2), Network Coprocessor (NETCP), and FFTC • Users can think of the Navigator as a mailbox mechanism with many additional and improved functions.

Intro to Multicore Navigator - Studylib

The Multicore Navigator is used to exchange control packets between the application and the SA firmware. Note: Due to export control restrictions the SA driver is a separate download from the rest of the MCSDK. ... See platform_library_user_guide located under C: ...

BIOS MCSDK 2.0 User Guide - Texas Instruments Wiki

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KeyStone Training Multicore Navigator Overview

The only book to offer special coverage of the fundamentals of multicore DSP for implementation on the TMS320C66xx SoC Manual setup of the clock (part 1) 172. 7.5.2 Lab 2: Manual setup of the clock (part 2) 172 ... 14 Multicore Navigator 491. 14.1 Introduction 491. 14.2 Navigator architecture 492.

Multicore DSP: From Algorithms to Real-time Implementation ...

Generate readable and portable C and C code from MATLAB ® code, including over 1,200 functions used for applications ranging from image processing and computer vision to advanced DSP and communications systems development, using MATLAB Coder™ .

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